

**REMARKS**

Applicant thanks the examiner for pointing out the allowable subject matter in claims 53-55 and 60-63.

Applicant respectfully submits that the non-elected claims 64-69 and 56-59 are patentable over prior art references at least for the same reasons that claims 60-63 are patentable over prior art references. Thus, Applicant respectfully submits that claims 64-69 and 56-59 are also allowable.

Claim 50 was rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent No. 5,288,949 (hereinafter "Crafts"). Applicant respectfully disagrees.

Claim 50 recites "wherein said shielding mesh is gridless"; and the Office Action relied upon Figure 6 of Crafts for this limitation. However, the power traces in Figure 6 of Crafts are in a perfect square grid, with a constant  $L/2$  (or  $L$ ) spacing in both the x-direction and the y-direction. For example, Crafts explicitly states:

"...in FIG. 6. These two sets of power traces form identical, but offset, grids. One grid is indicated by the solid shading, and the other by stippled shading. The offset is related to the spacing  $L$  between the individual conductors in each grid. That is, the grids are offset by distance  $L/2$  in both the x-direction and the y-direction, as indicated" (Col. 2, lines 49-56, Crafts).

Thus, the power traces of Crafts are not gridless.

Further, the traces of Crafts are not in layers of an Integrated Circuit (IC). The traces of Crafts are in a carrier for ICs, such as a Multi-layer Ceramic Module (MCM), which interconnects the ICs (see, e.g., Figure 3A; Col. 2, lines 19-22, Crafts).

Thus, claim 50 is patentable over Crafts.

Claims 51 and 52 are dependent claims of claim 50. Claims 51 and 52 are patentable over Crafts and the alleged knowledge in the art at least for the reasons discussed above for claim 50.

Thus, Applicant respectfully submits that the pending claims are all allowable.

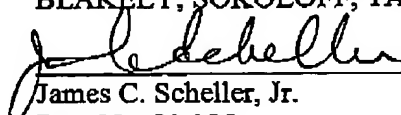
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Furthermore, if an extension is required, Applicant hereby requests such extension.

Respectfully submitted,

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